

Systemutformning i multisystemmiljö

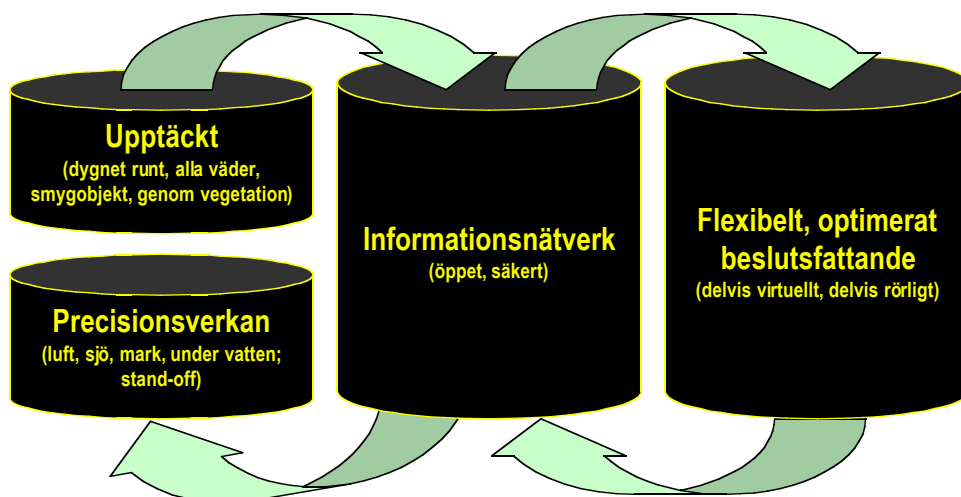
**Peter Holmström,
Chef SYSTEM,
FMV SYSTEMLEDNING**

**SYSTEM
LEDNING**

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RMA

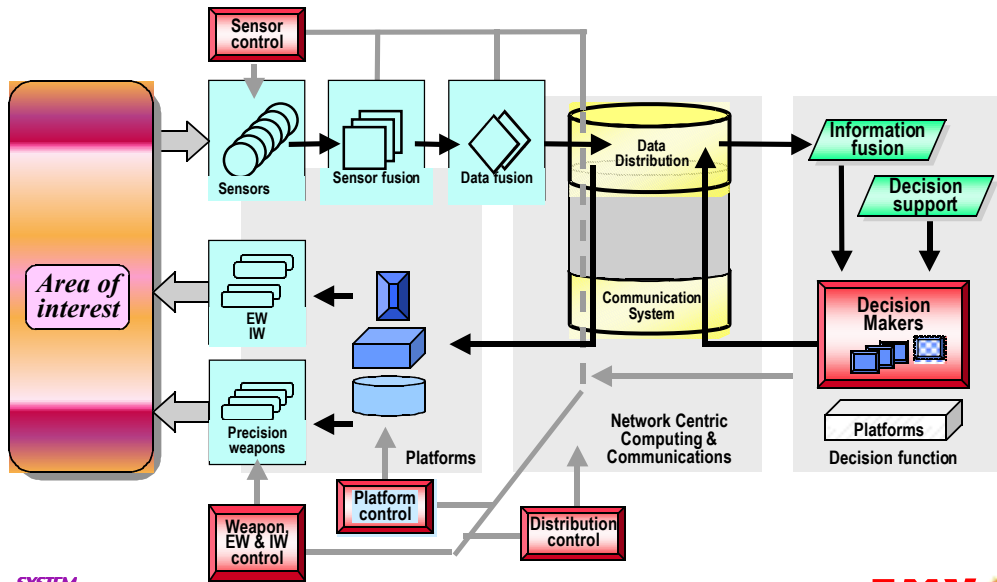


**SYSTEM
LEDNING**

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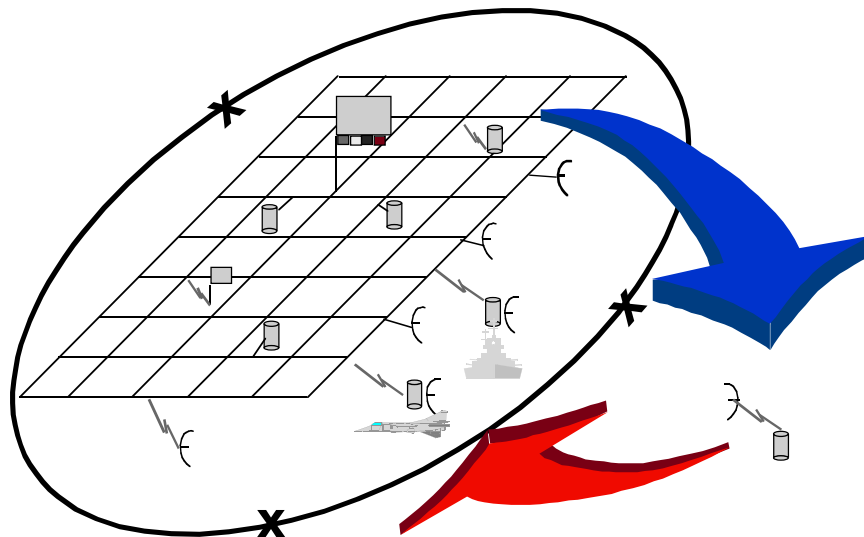


Den nya tekniska strukturen



SYSTEM
LEDNING

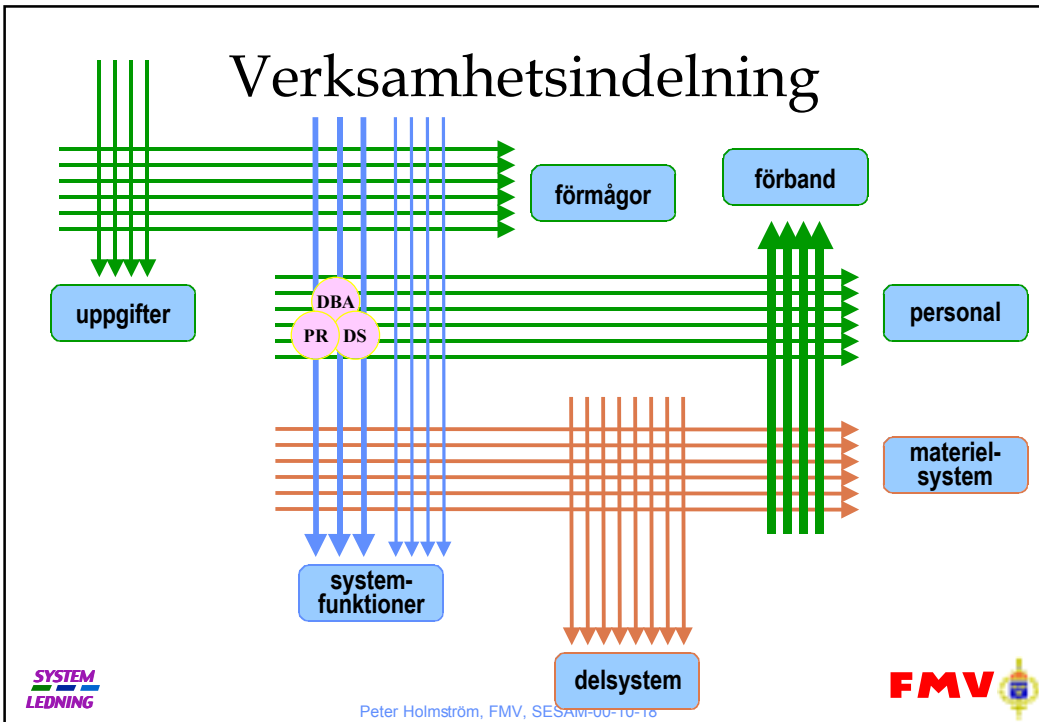
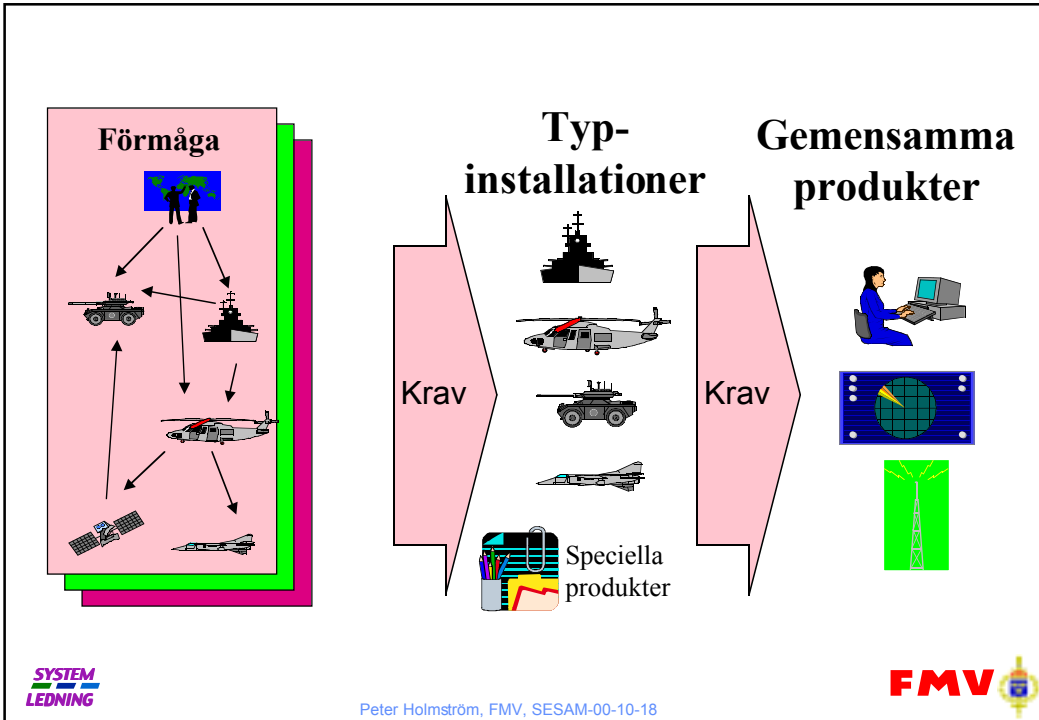
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SYSTEM
LEDNING

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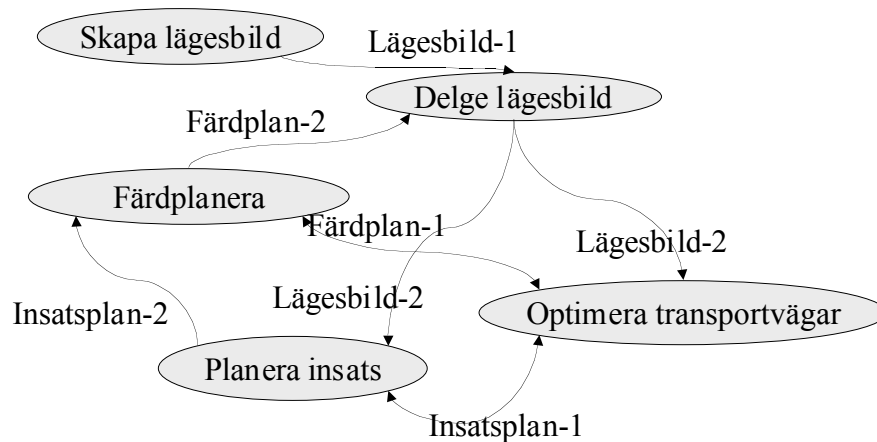


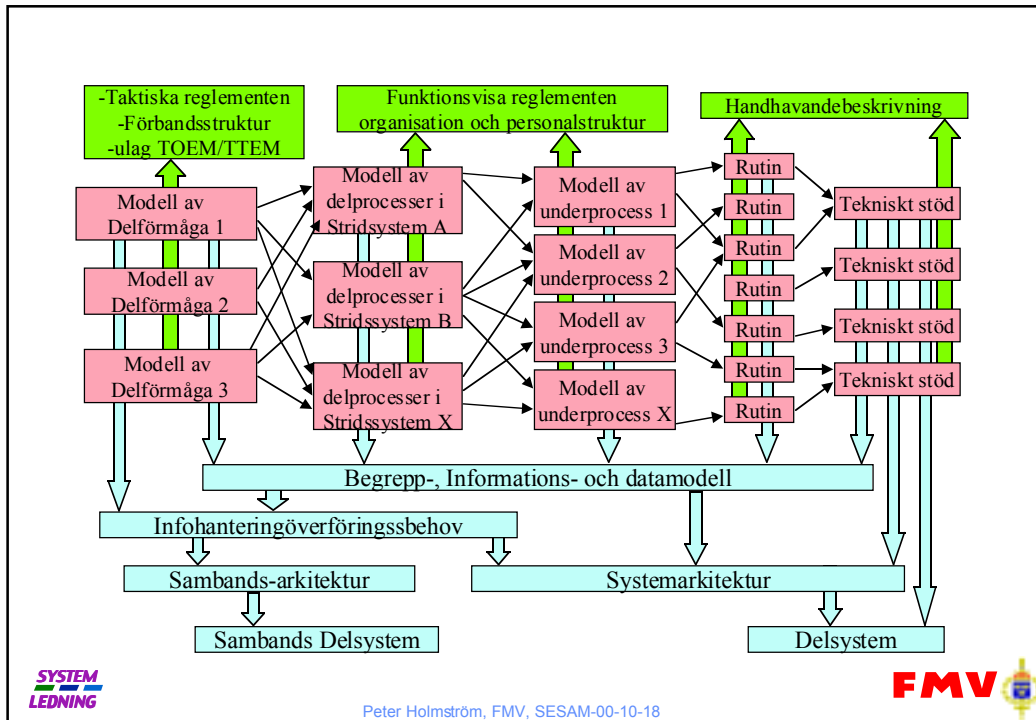


Förmåge- Process- System- Matris

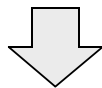
Operativa/Taktiska Förmågor	Grupp M				Grupp S				Grupp F			
	M1	M2	M3	M4	S1	S2	S3	S4	F1	F2	F3	F4
Processer/Delprocesser												
Fsrbandsorder	x	x	x	x	x	x	x	x	x	x	x	x
Stridsorder	x	x	x	x								
Indirekt eld	x		x					x				
Direkt eld			x									
SensorspessammanstSlining	x		x					x		x		
Sensorspesspridning	x	x	x	x	x	x	x	x	x	x	x	x
Rapportering efter strid	x		x					x		x		
ResursSpesrapportering	x	x	x	x	x	x	x	x	x	x	x	x
Fsrdplanering	x			x		x			x	x		
Ingående Fsrband/System (nivå 2/3)												
Kv Visby	x	x						x	x	x		
Stv 101	x	x	x	x				x	x	x		
Jas 39	x	x			x		x			x		
Jas 39B		x				x		x		x		
TARAS						x						
FTN	x				x					x		
MTS				x								
MARKOV		x	x					x				
LuLIS			x				x		x			
SjsstridskiSdsel	x	x	x								x	

Förmågor realiseras genom användning
av flera funktioner/processer/användningsfall





Arkitektur

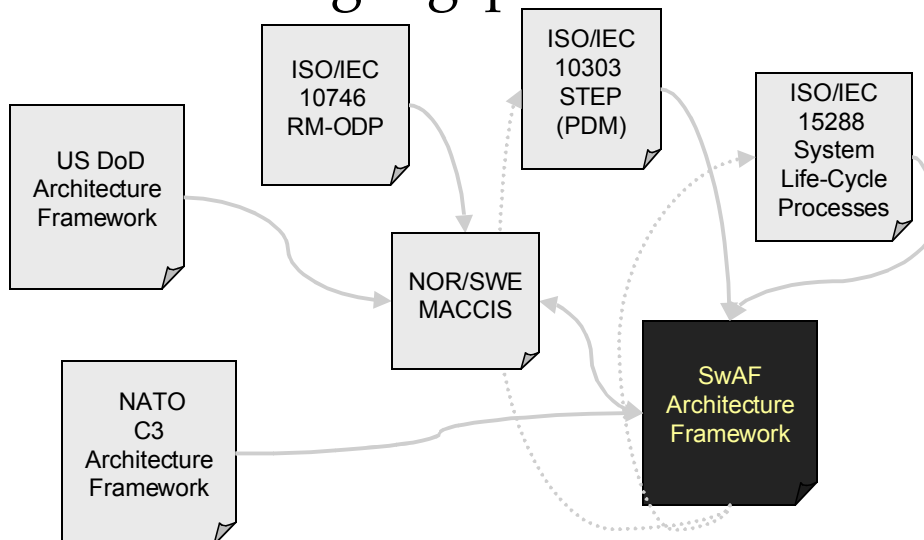


**Hur skapar vi delar
så att dessa sedan kan
byggas ihop till en helhet**

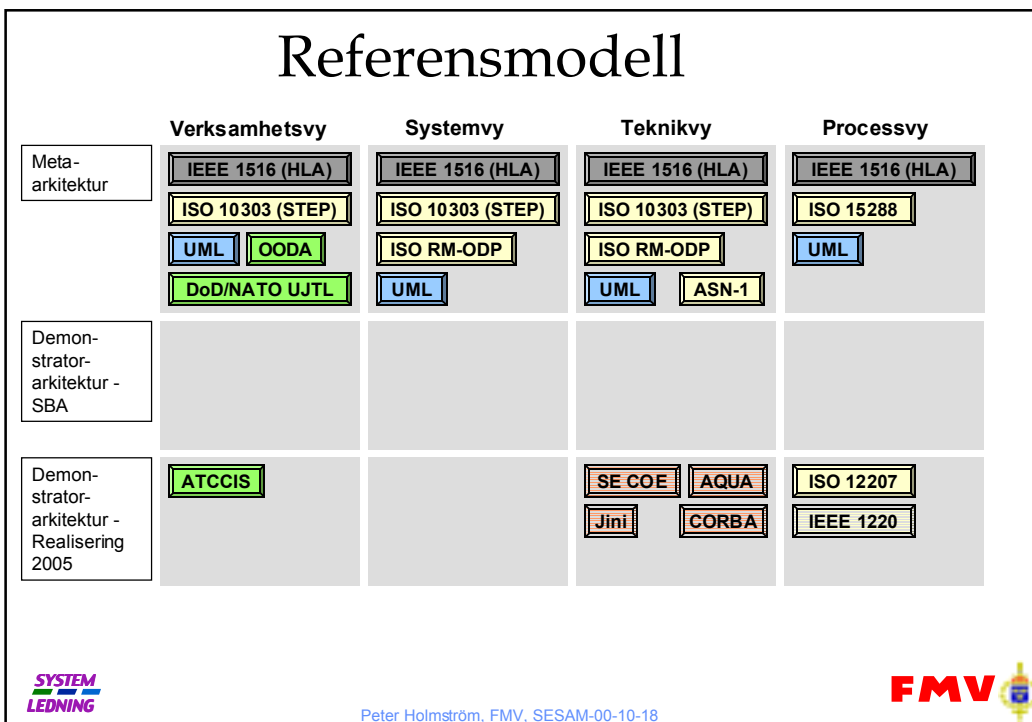
Arkitekturer



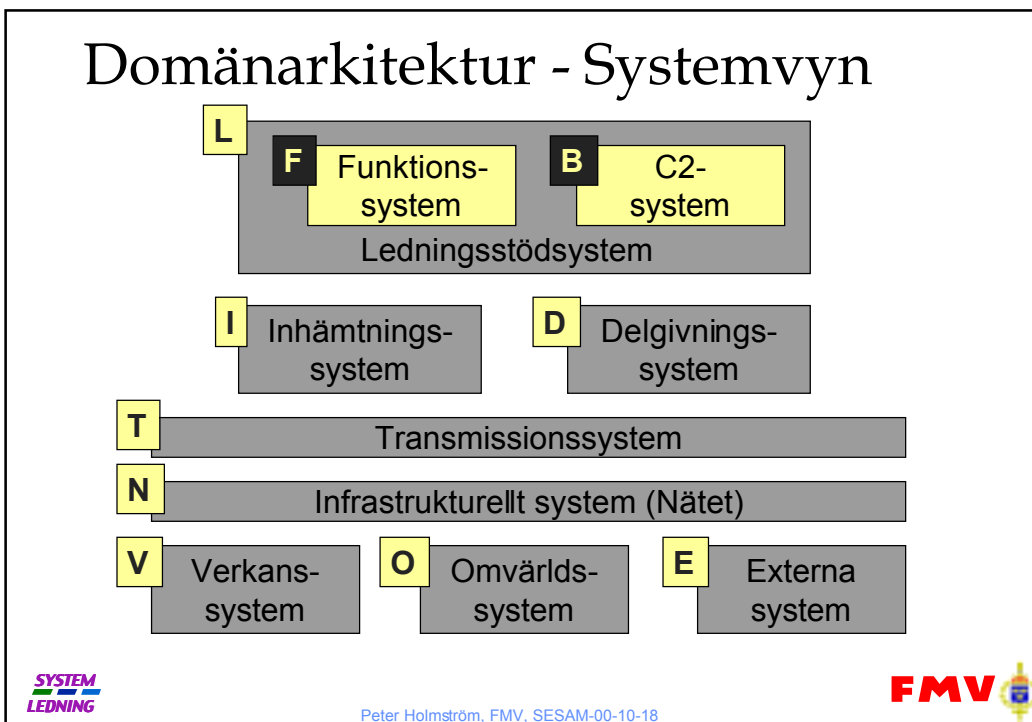
Utgångspunkter



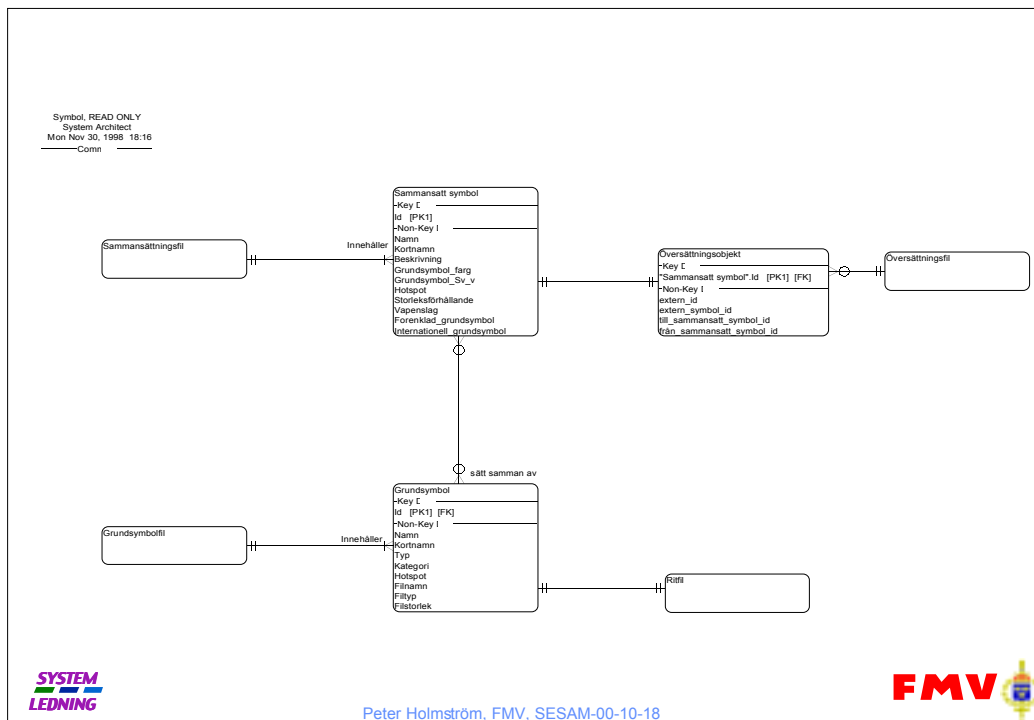
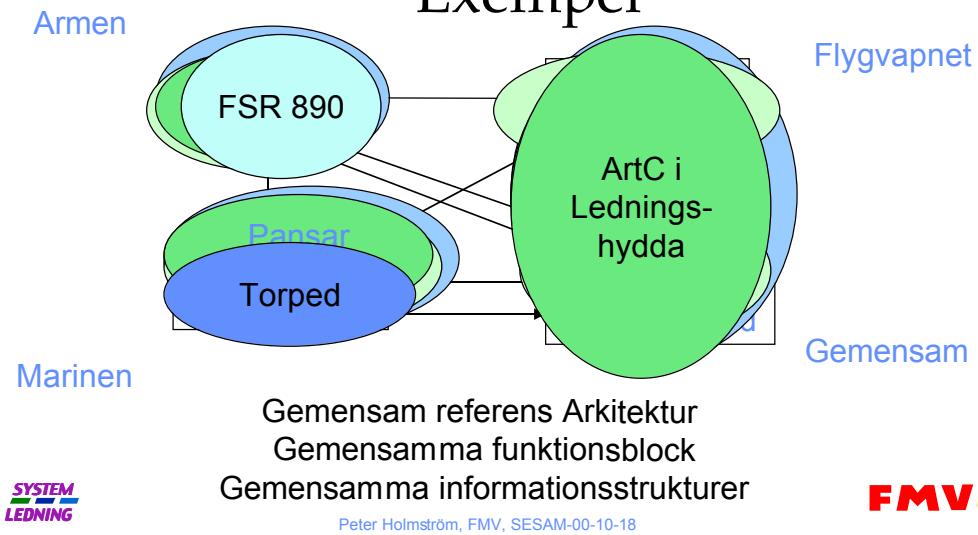
Referensmodell



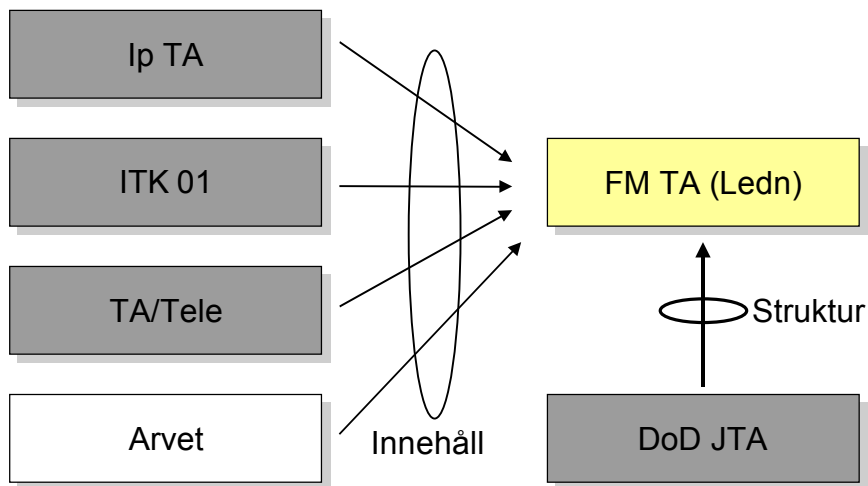
Domänarkitektur - Systemvyn



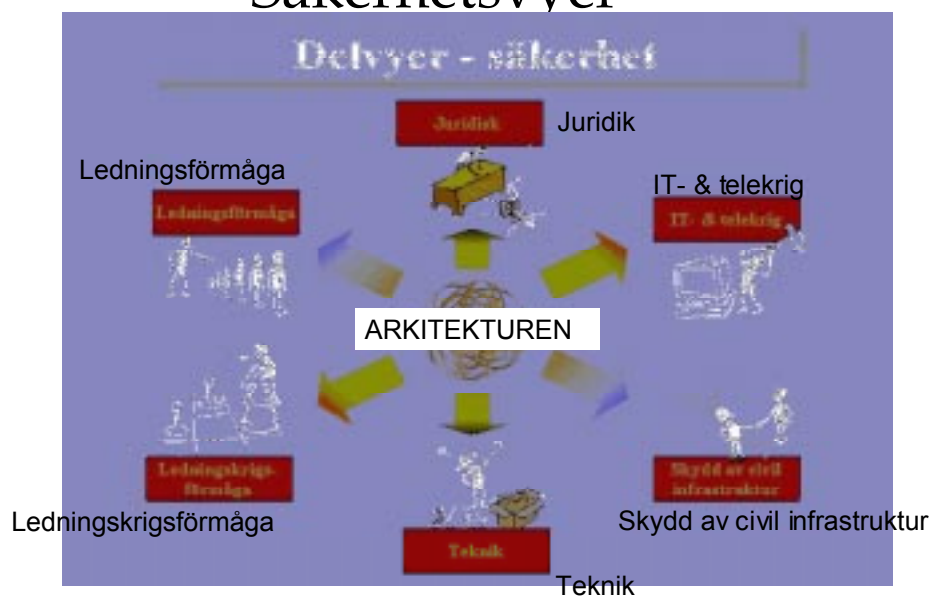
Implementering : System-Arkitektur, Exempel



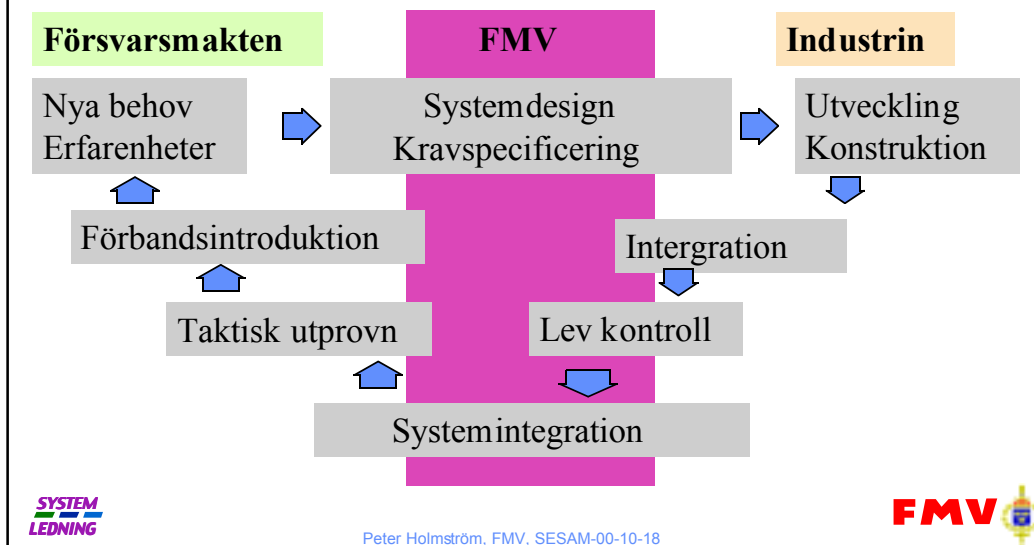
Teknisk Arkitektur



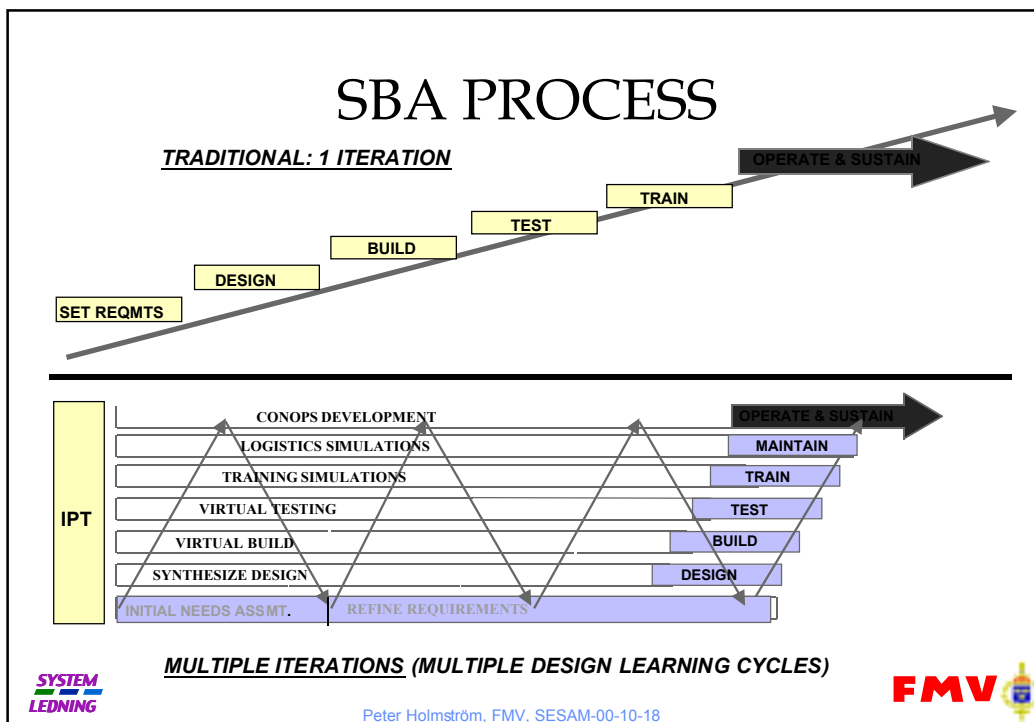
Säkerhetsvyer



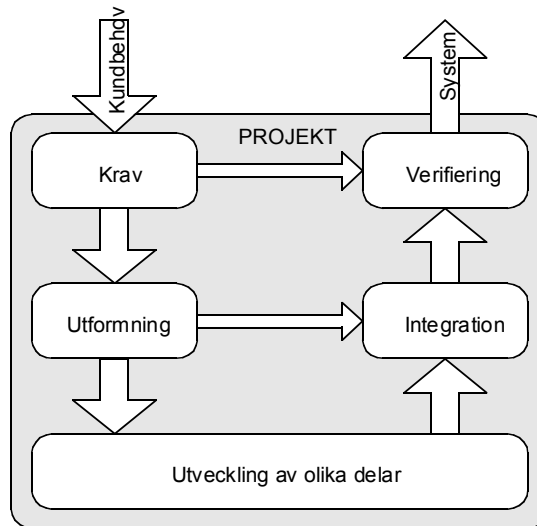
Utvecklingsmodell



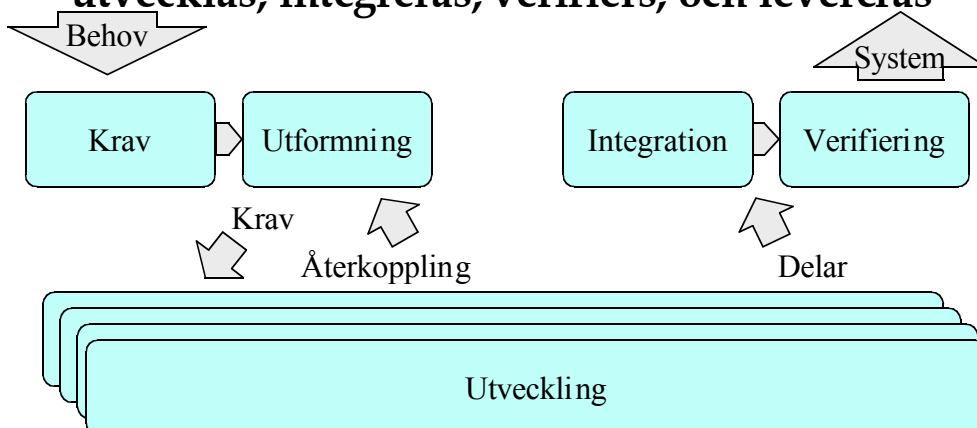
SBA PROCESS

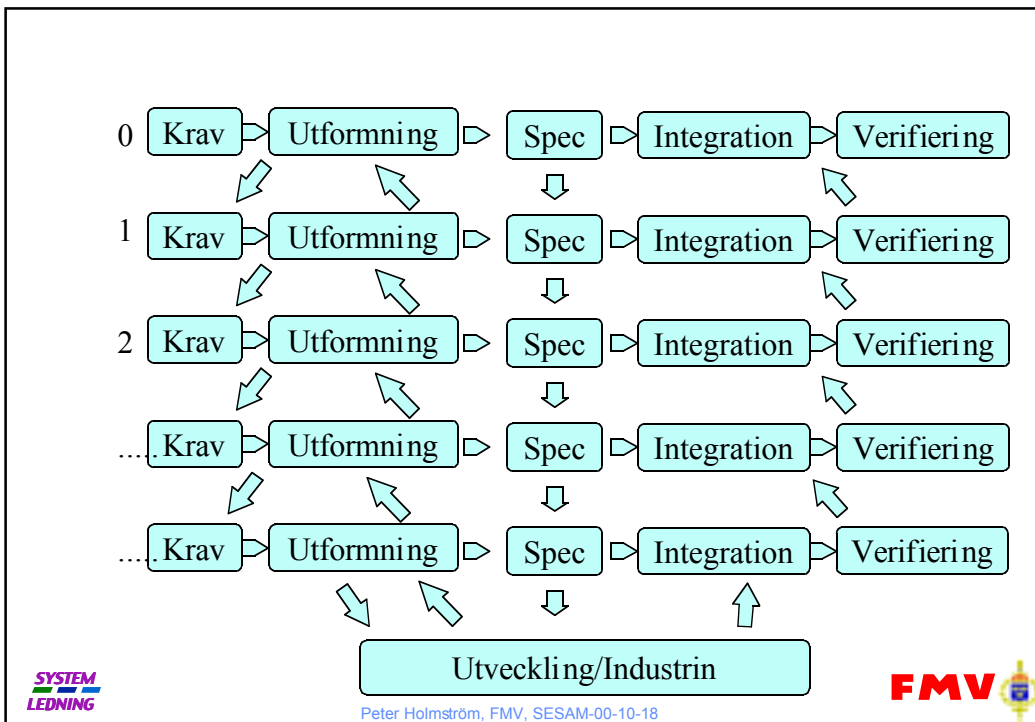
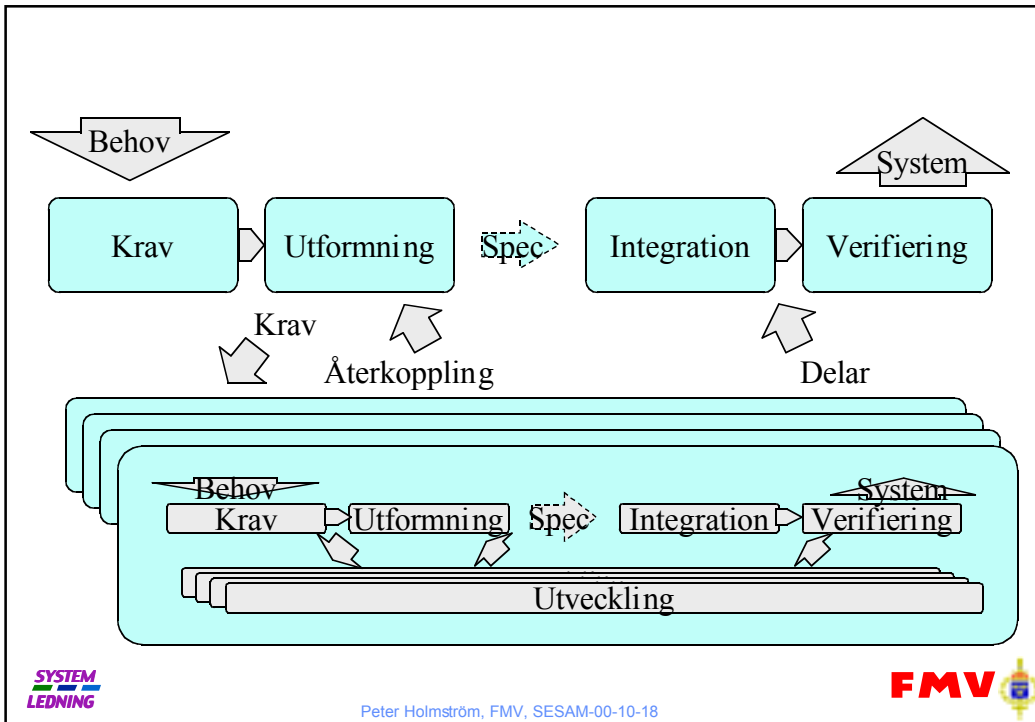


Ett system eller en produkt kravställs, utformas, utvecklas, integreras, verifieras och levereras

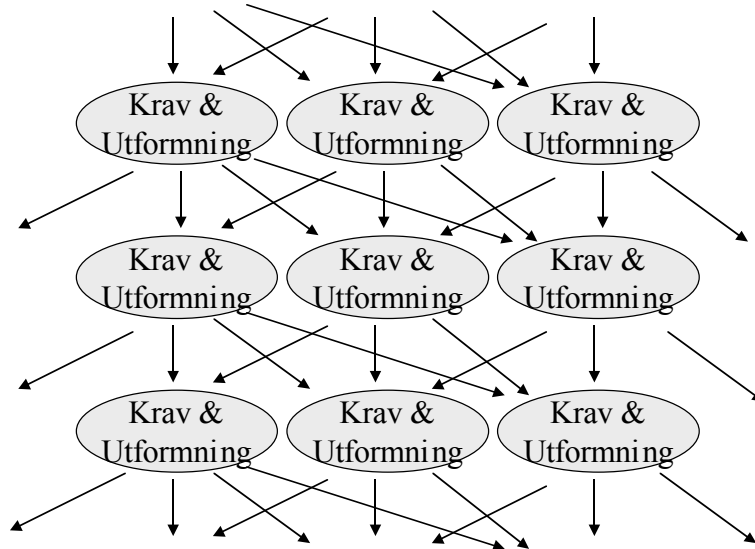


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Multipla krav- relationer

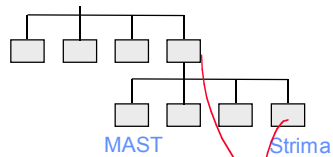


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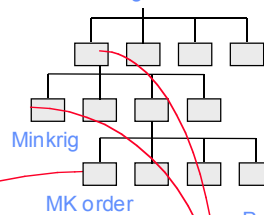


Konfigurationsstyrning

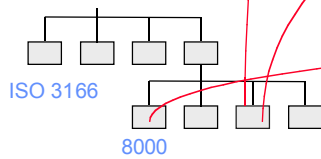
Produktstruktur av gemensamma produkter och delsystem



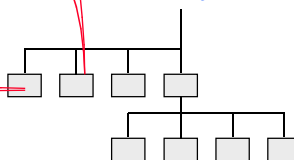
Förmåge & funktionsbeskrivning i Processer



Tekniska Policys



Produktstruktur av nivå2 system



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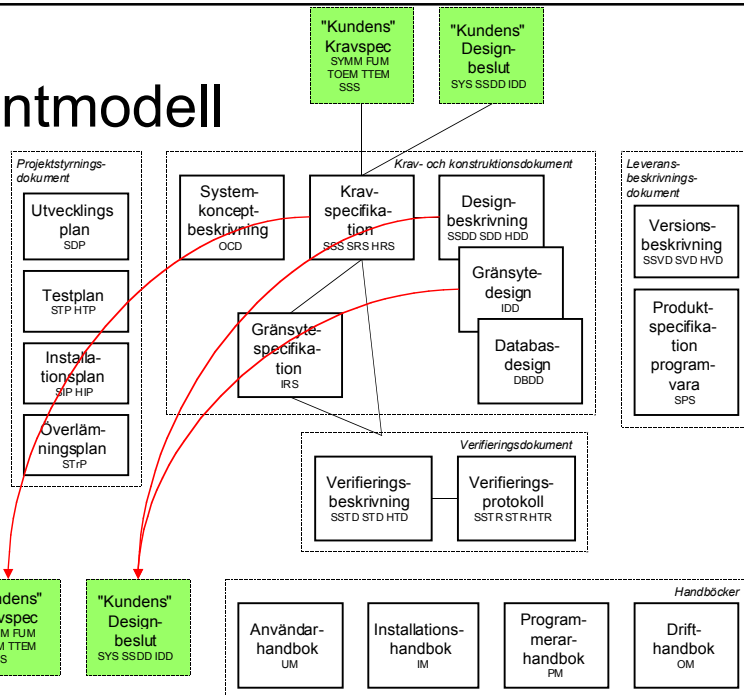




Dokumentmodell

Dokumentstruktur baserad på J-std-016 & IEEE1498

Anpassad för Systemutveckling



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Dokumentstruktur för produkter

IEEE 1498

Gammal men ej ersatt



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DERAmodellen i stort, (IEEE 12207 & 15288)

RUP i smått



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